Part 1) This program uses an lpm module for extra ram

Part 2) This program uses an lpm module for extra ram and uses the DE115 board to test the ram block

Part 3) This program uses a 2 dimensional array to emulate a RAM module

Part 4) This program uses SRAM to replicate the results in Lab82 and Lab83. The only issue with this implementation is that the data can still be written one time after SW[17] is brought low